

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L21	1513	714/800-802.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/13 10:07
L22	1	L21 and L17	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/13 10:08
S1	2	"20050132213"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/10 09:46
S2	5078	(I2C or (inter integrated circuit) or (interintegrated circuit) or (inter-integrated circuit) or i-squared-c)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/10 13:29
S3	19	S2 and (push or pull) near2 driver\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/10 09:52
S4	25173516	@AD<"20031210"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/10 09:53
S5	16	S3 and S4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/10 09:54

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S6	2337	S2 and S4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/10 16:14
S7	41	S6 and (low voltage with differential)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/10 10:01
S8	36	(low voltage differential signal\$4 or LVDS) and S6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/10 11:24
S9	16	((("6535519") or ("5929655") or ("5721703") or ("5703759") or ("4935734") or ("4805137") or ("4426637") or ("4387397"))). PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2007/04/10 10:38
S10	0	S2 and S9	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/10 10:38
S11	765	(low voltage differential signal\$4 or LVDS) and (single end\$2 or single-end\$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/10 11:01
S12	51	S11 and S2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/10 10:45
S13	7	S11 and S6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/10 10:45

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S14	6	((("6567879") or ("6715019") or ("6748477")).PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2007/04/10 11:03
S15	3	"7106103"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/10 11:10
S16	14	((("6973597") or ("6825389") or ("6778526") or ("6608500") or ("6271679") or ("6218858") or ("6034542")).PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2007/04/10 11:10
S17	94	(single end\$2 or single-end\$2) and S6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/12 11:51
S19	206	S6 and bus controller\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/10 12:09
S20	8	S19 and ((push or pull) with driver\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/10 12:12
S21	35	S6 and ((push or pull) with driver\$1) not S20	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/10 12:36
S22	17	S6 and push-pull	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/10 12:30

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S24	36	S6 and multiple (IC\$1 or integrated circuit\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/10 12:36
S25	97	(I2C or (inter integrated circuit) or (interintegrated circuit) or (inter-integrated circuit) or i-squared-c) controller\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/10 12:50
S26	75	S25 and S4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/10 12:42
S27	1	((I2C or (inter integrated circuit) or (interintegrated circuit) or (inter-integrated circuit) or i-squared-c) controller\$1 same (clock signal\$1)) and S4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/10 12:57
S28	895	push-pull driver\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/10 12:57
S29	32	S28 with (data or clock) signal\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/10 14:19
S30	23	S29 and S4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/10 12:58
S31	22270	(I2C or (inter integrated circuit) or (interintegrated circuit) or (inter-integrated circuit) or i-squared-c or two-wire)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/10 13:03

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S32	14522	S31 and S4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/10 13:04
S34	17	((I2C or (inter integrated circuit) or (interintegrated circuit) or (inter-integrated circuit) or i-squared-c) with (controller\$1 or manager\$1) with (clock or data) signal\$1) and S4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/10 13:11
S35	33	((I2C or (inter integrated circuit) or (interintegrated circuit) or (inter-integrated circuit) or i-squared-c) bus (controller\$1 or manager\$1))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/10 13:12
S36	27	S35 and S4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/10 13:12
S37	0	S6 and DPTY	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/10 13:29
S38	705	S6 and clock signal\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/10 13:29
S39	118	S6 and clock signal\$1 with (SCL or clock (bus or lane or link))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/10 13:33
S40	2	S6 and (dedicated or permanent) master	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/10 13:33

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S41	317	710/106.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/10 14:19
S42	690	710/110.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/10 14:19
S43	31	S6 and (S41 or S42)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/10 14:20
S44	5078	(I2C or (inter integrated circuit) or (interintegrated circuit) or (inter-integrated circuit) or i-squared-c)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/10 16:14
S45	25173516	@AD<"20031210"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/10 16:14
S46	2337	S44 and S45	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/12 08:55
S47	17	S46 and (push-pull)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/10 16:15
S48	5084	(I2C or (inter integrated circuit) or (interintegrated circuit) or (inter-integrated circuit) or i-squared-c)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/12 10:24

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S49	25173587	@AD<"20031210"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/12 08:55
S50	2337	S48 and S49	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/13 09:07
S51	3	S50 and print\$3 device	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/12 09:29
S52	286	S50 and printer\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/12 09:35
S53	2	S50 and (printer\$1).ti.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/12 09:35
S54	30	S48 same (printer\$1 or copier\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/12 09:44
S55	94	S50 and (single-ended or single ended)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/12 10:29
S56	36	S50 and (low voltage differential signal\$3 or LVDS)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/12 10:17

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S57	7	S55 and S56	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/12 11:33
S58	14	S48 same (low voltage differential signal\$3 or LVDS)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/12 11:12
S59	2	S48 same (single-ended or single ended)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/12 10:29
S60	98	S48 same differential	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/12 11:12
S61	35	S60 and S50	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/12 11:15
S62	18	S48 and current loop	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/12 11:15
S63	2	S50 and current loop	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/12 11:15
S64	33	S50 and differential (clock or data)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/12 11:33

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S65	5084	(I2C or (inter integrated circuit) or (interintegrated circuit) or (inter-integrated circuit) or i-squared-c)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/12 11:51
S66	25173587	@AD<"20031210"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/12 11:51
S67	2337	S65 and S66	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/12 11:51
S68	94	(single end\$2 or single-end\$2) and S67	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/12 11:54
S69	2	(single end\$2 or single-end\$2) same S48	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/12 11:55
S71	99197	((application specific or application-specific) integrated circuit) or ASIC	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/12 14:35
S72	5084	(I2C or (inter integrated circuit) or (interintegrated circuit) or (inter-integrated circuit) or i-squared-c)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/12 14:35
S73	25173587	@AD<"20031210"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/12 14:35

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S74	2337	S72 and S73	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/12 14:35
S75	440	S74 and S71	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/12 14:39
S76	70	S75 and printer\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/12 14:40
S77	46	S72 with S71	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/12 14:46
S78	62	S72 and (master with S71)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/12 14:48
S79	21	S78 and S74	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/12 15:58
S80	73	control parity bit\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/12 16:08
S81	1193	data parity bit\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/12 16:09

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S82	12	S80 and S81	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/12 16:05
S83	25173587	@AD<"20031210"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/12 16:05
S84	10	S82 and S83	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/12 16:05
S85	5084	(I2C or (inter integrated circuit) or (interintegrated circuit) or (inter-integrated circuit) or i-squared-c)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/12 16:08
S93	109	708/531.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/12 16:17
S94	0	S93 and S85	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2007/04/12 16:18